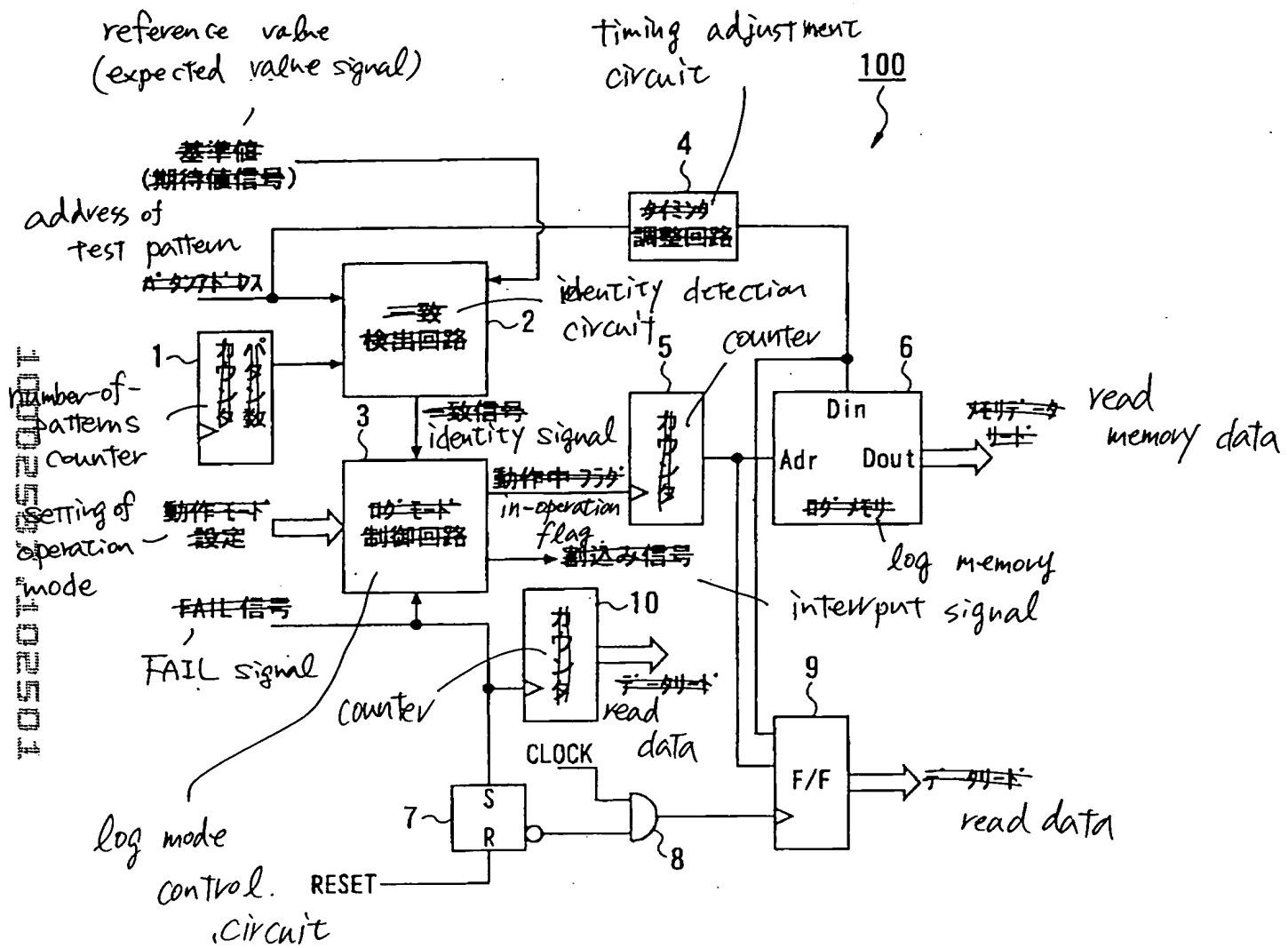


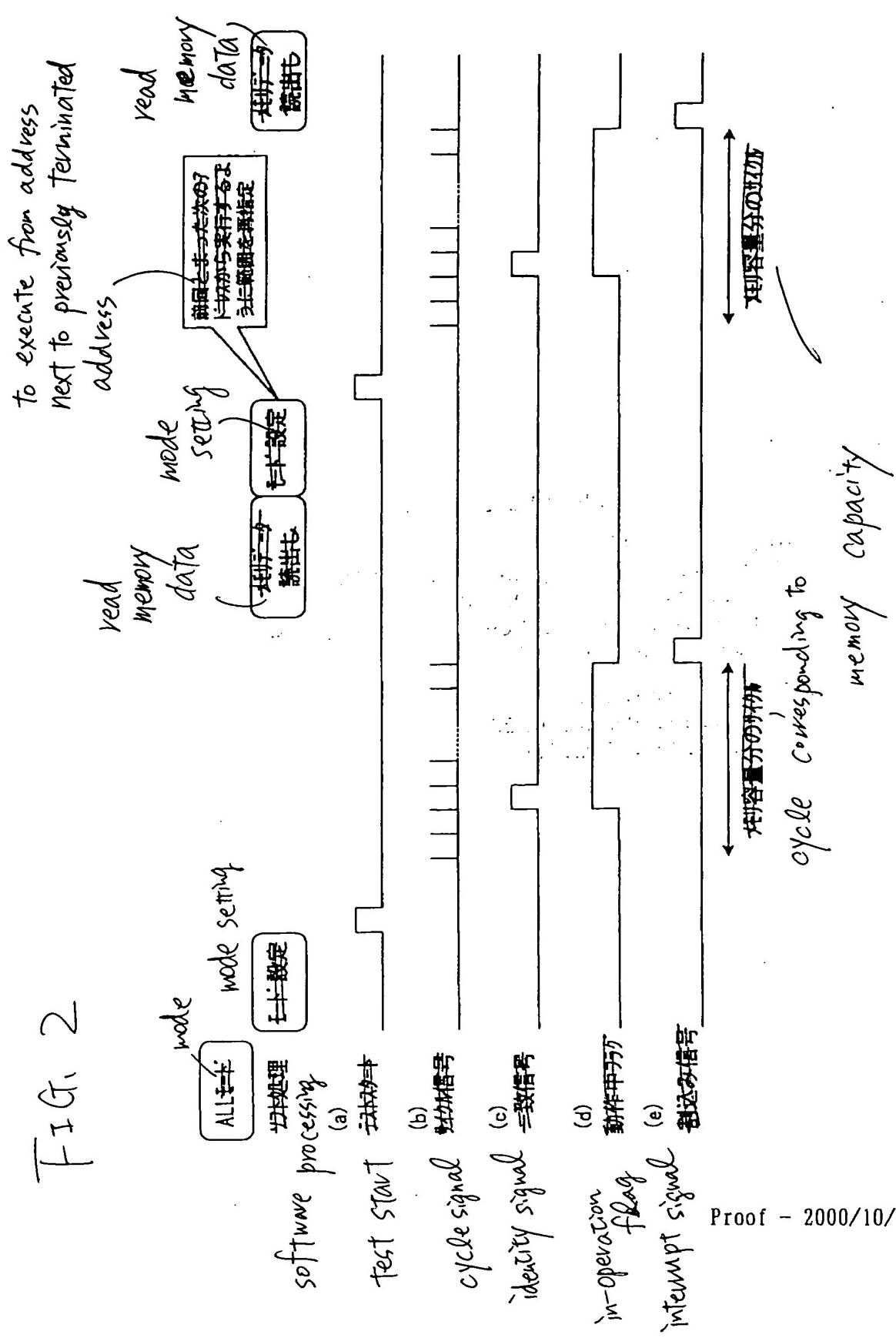
FIG. 1

1/5



10002587 - 102501
reset range

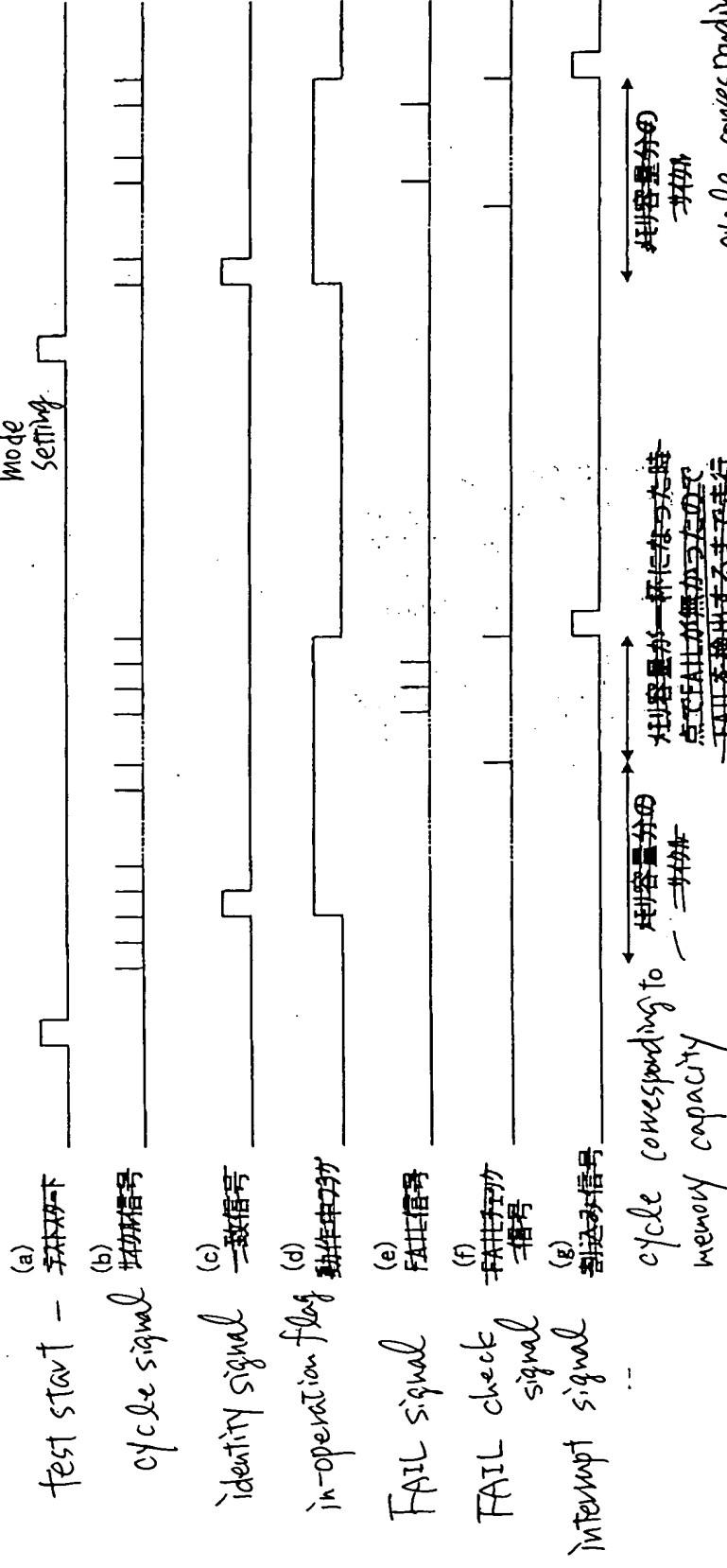
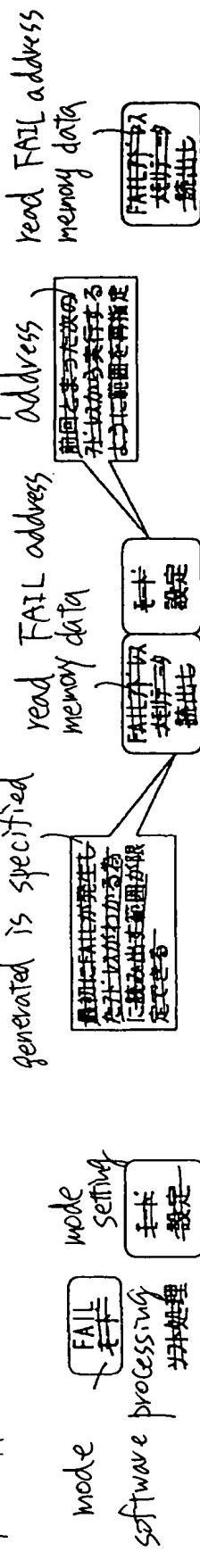
FIG. 2



Proof - 2000/10/30

FIG. 3

range to be read "FAIL" be generated OUT because address at which FAIL was first generated is specified



FAIL is detected because there was no FAIL at the point of time when memory capacity had been occupied

cycle corresponding to
memory capacity

W
5

FIG. 4

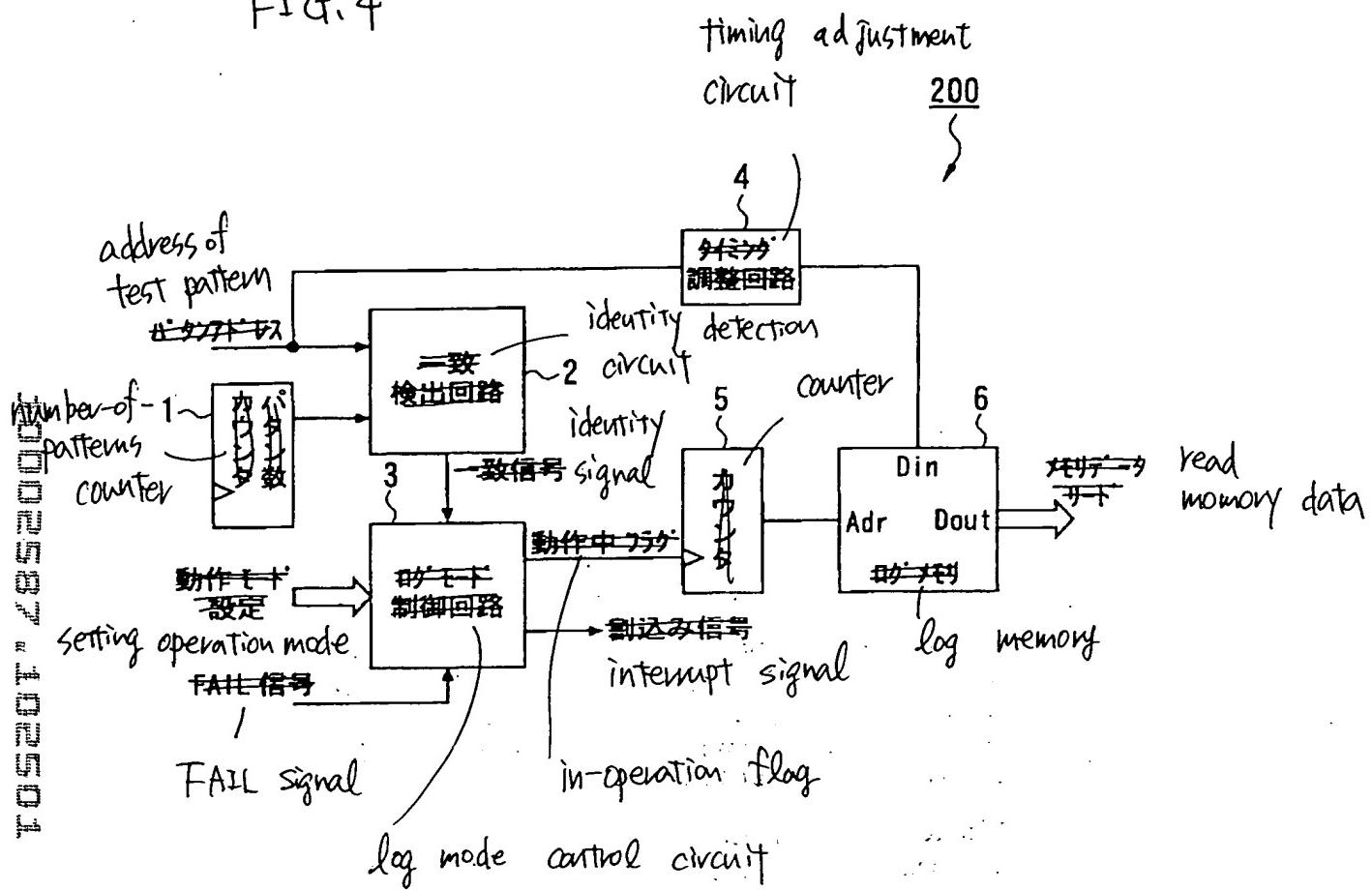


Fig. 5

